



Developments in Surface Contamination and Cleaning - Vol 7: Cleanliness Validation and Verification

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As device sizes in the semiconductor industries are shrinking, they become more vulnerable to smaller contaminant particles, and most conventional cleaning techniques employed in the industry are not as effective at smaller scales. The book series *Developments in Surface Contamination and Cleaning* as a whole provides an excellent source of information on these alternative cleaning techniques as well as methods for characterization and validation of surface contamination. Each volume has a particular topical focus, covering the key techniques and recent developments in the area. The chapters in this Volume address the sources of surface contaminants and various methods for their collection and characterization, as well as methods for cleanliness validation. Regulatory aspects of cleaning are also covered. The collection of topics in this book is unique and complements other volumes in this series. Edited by the leading experts in small-scale particle surface contamination, cleaning and cleaning control, these books will be an invaluable reference for researchers and engineers in R&D, manufacturing, quality control and procurement specification situated in a multitude of industries such as: aerospace, automotive, biomedical, defense, energy, manufacturing, microelectronics, optics and xerography.

This Volume complements other volumes in this series and:

- Provides a state-of-the-art survey and best-practice guidance for scientists and engineers engaged in surface cleaning or handling the consequences of surface contamination
- Addresses the continuing trends of shrinking device size and contamination vulnerability in a range of industries, spearheaded by the semiconductor industry and others
- Includes new regulatory aspects

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